QUALCOMM Incorporated, MOHSENI, et al. 010085 SYSTEM METHOD AND CAPPARATUS FOR FRACTIONAL DELAY EL903006608 US

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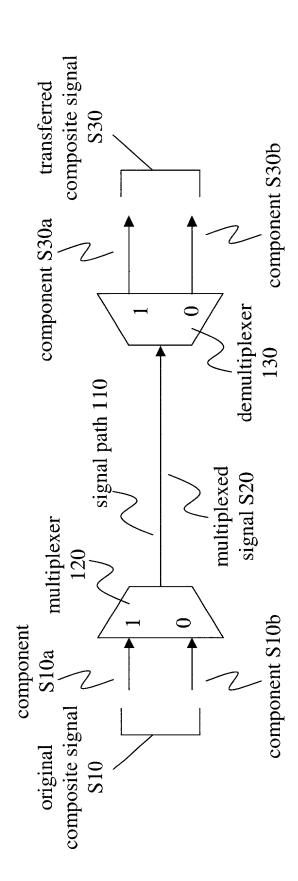


FIG.

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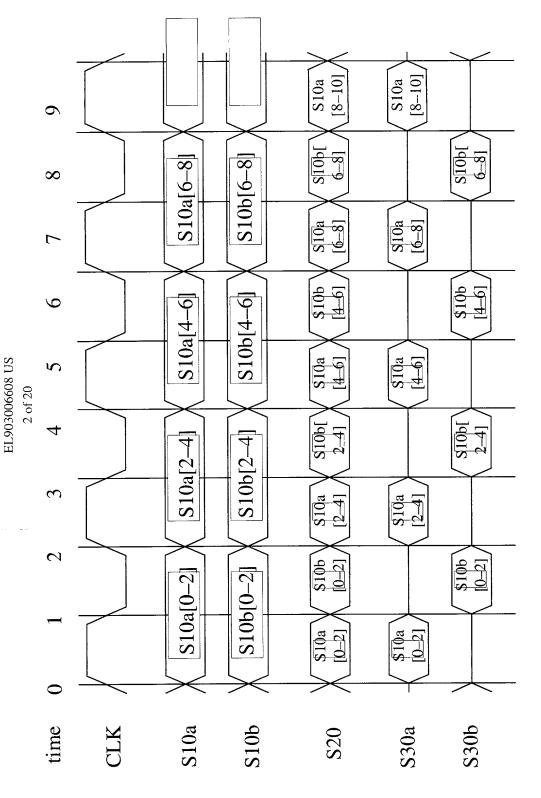


FIG. 2

APPARATUS FOR FRACTIONAL DELAY

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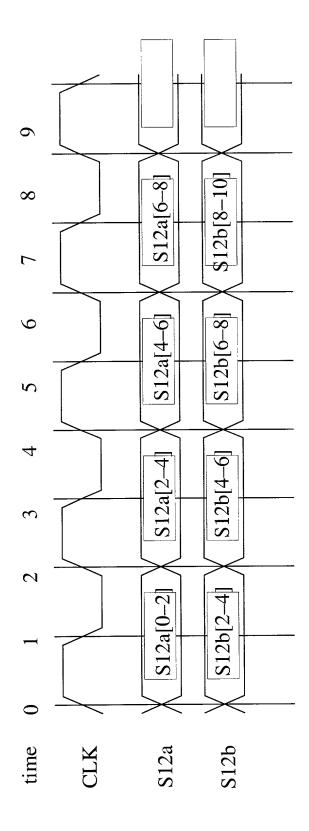


FIG.

APPARATUS FOR FRACTIONAL DELAY
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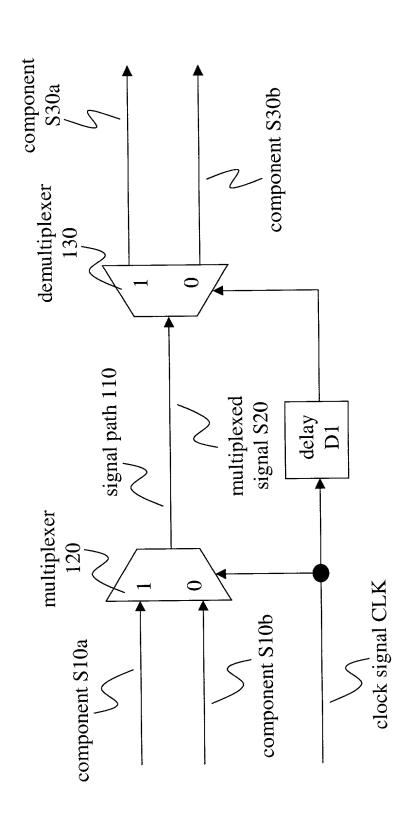


FIG. 4

APPARATUS FOR FRACTIONAL DELAY
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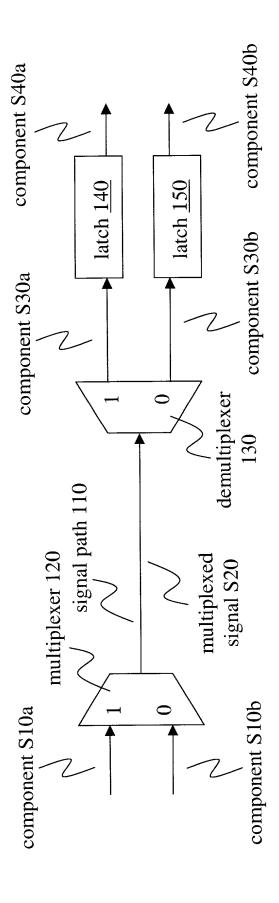


FIG. 5

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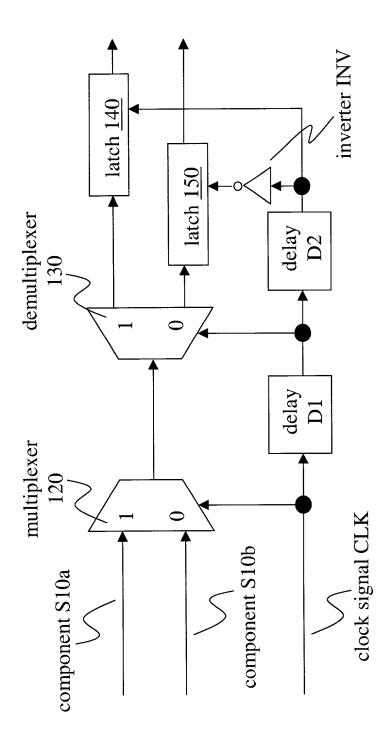


FIG. 6

S10a [8–10] \$10b[6-8]6 S10b[ 6-8] S10a[6-8]S10b[6-8] S10a[6-8  $\infty$ S10aS10b[4-6]\$10b [4\_6] [S10b[4-6]]S10a[4-6]S10a[4-6]9 \$10a [4<u>-</u>6] S S10b[2-4]7 of 20 \$10b[ 2\_4] 4 S10b[2-4] S10a[2-4 S10a[2-4 S10a [2-4] 3 S10b[0-2 2 \$10b [0-2] S10b[0-2]S10a[0-2 S10a[0-2]\$10a [0<u>-</u>2] S40b time S10a S10bS30a S30b S40a

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FIG. 7

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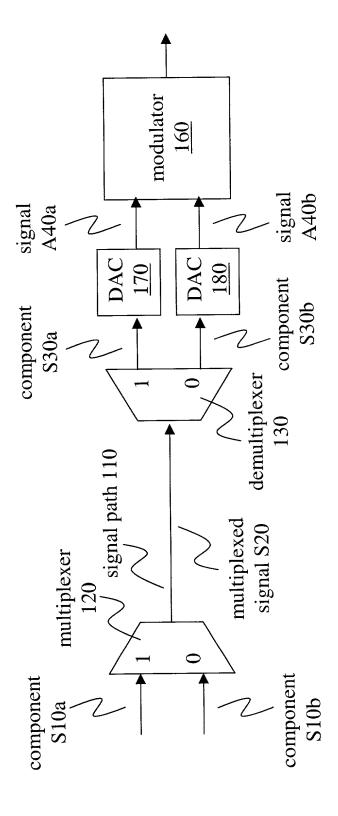


FIG. 8

QUALCOMM Incorporated, MOHSENI, et al.
The state of the s

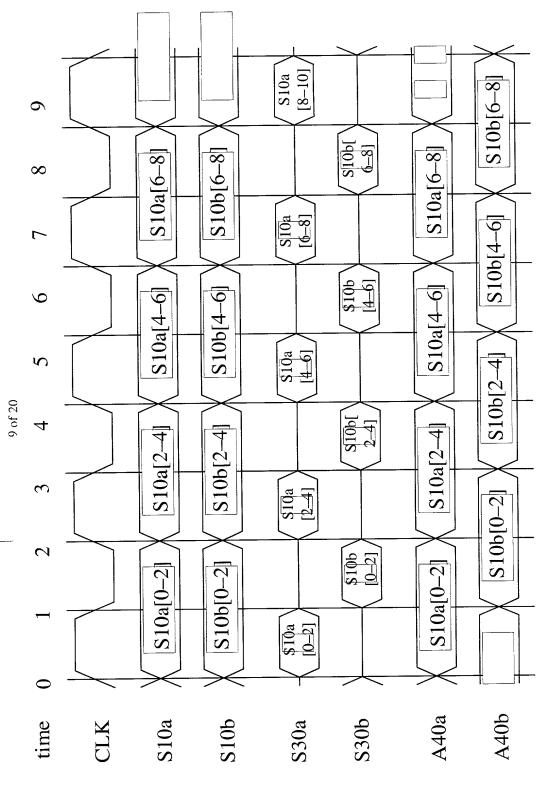


FIG. 6

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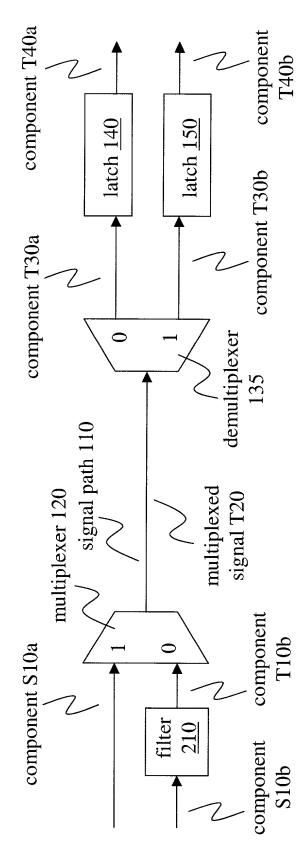


FIG. 10

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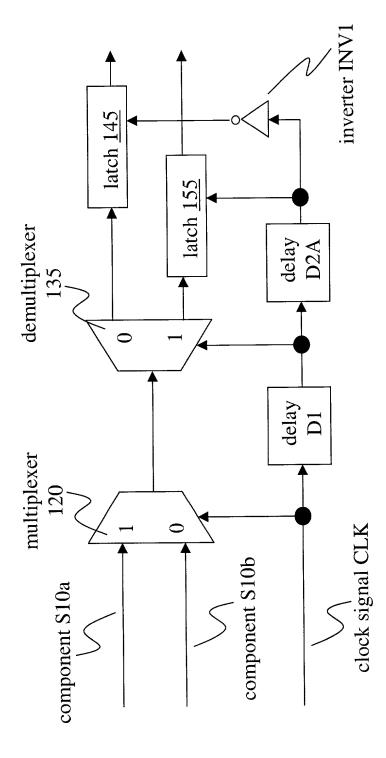


FIG. 11

QUALCOMM Incorporated, MOHSENI, et al. 010085. SYSTEM. METHOD. AND WIPPARATUS FOR FRACTIONAL DELAY EL903006608 US

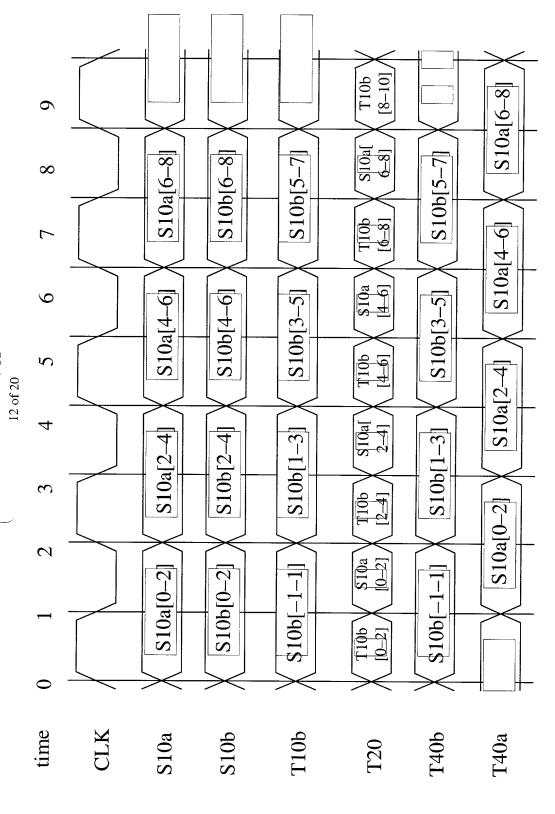


FIG. 12

APPARATUS FOR FRACTIONAL DELAY
EL903006608 US

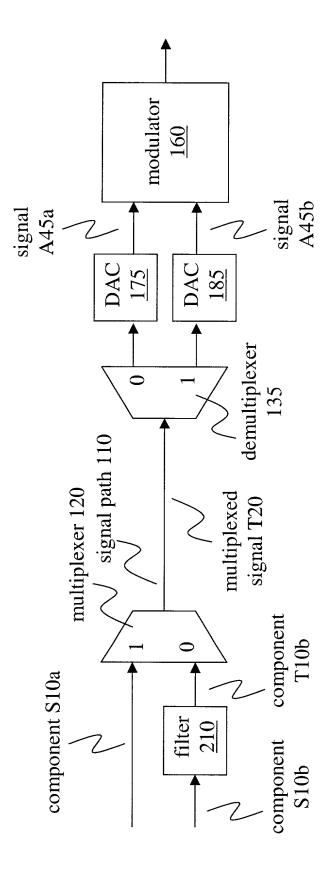


FIG. 13

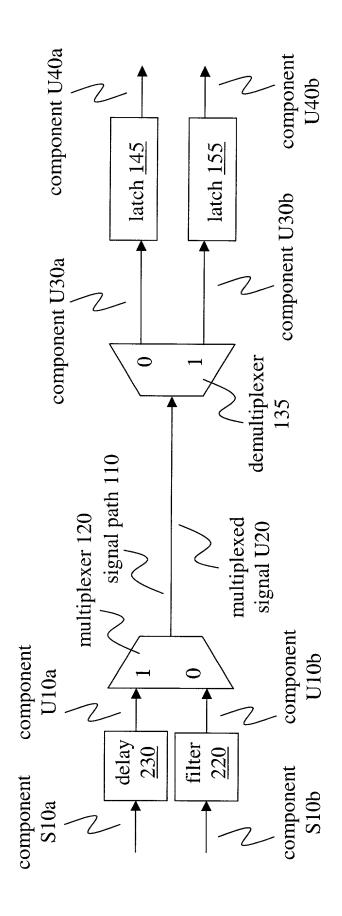


FIG. 14

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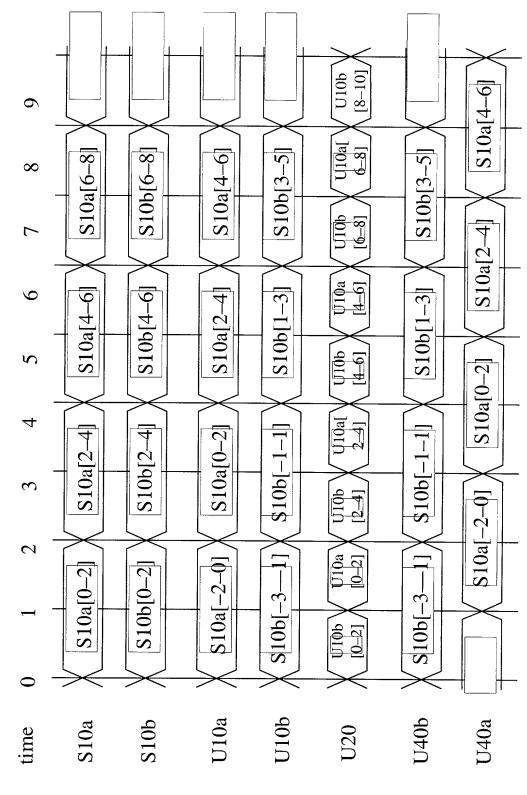


FIG. 15

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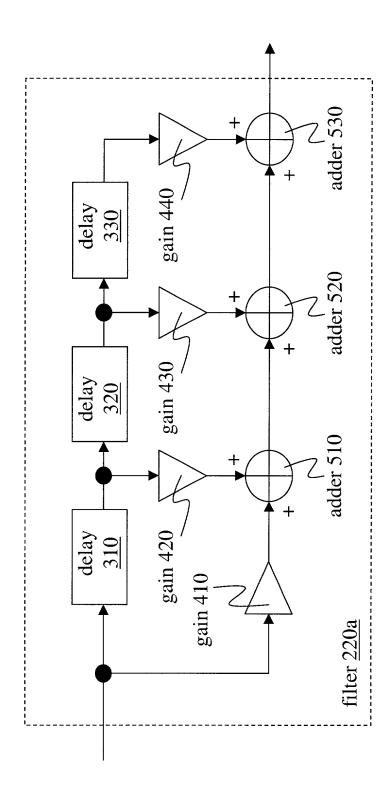


FIG. 16

delay 330 adder 560 QUALCOMM Incorporated, MOHSENI, et al. The Colon of the C EL903006608 US shifter delay 320610 17 of 20 adder 570 adder 550 delay + 310 filter 220b adder 540

FIG. 17

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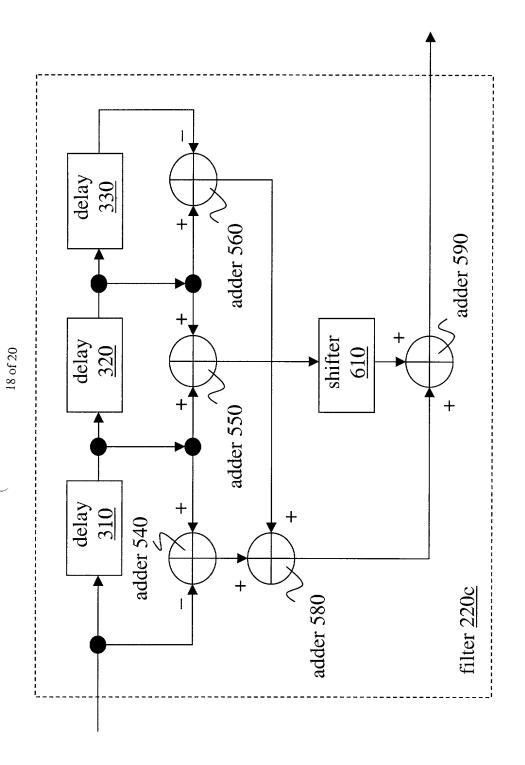


FIG. 18

QUALCOMM Incorporated, MOHSENI, et al. 010085 SYSTEM, METHOD AND F. L. L. MEPARATUST CRITICAL DELAY EL903006608 US

